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Patent
10/712,810

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Blanchard et al.

Serial No.: 10/712,810

Filed: November 13, 2003

Title: Method For Fabricating A Power Semiconductor Device Having A Voltage Sustaining Layer With A Terraced Trench Facilitating Formation Of Floating Islands

Art Unit: 2814

Confirmation No.: 8845

Examiner: Thao X. Le

Docket No.: GS 160 D1

Via Facsimile: 571-273-8300
Commissioner for Patents
P.O. Box 1450
Washington, D.C. 22313-1450RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated May 15, 2007, kindly consider the following remarks:

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this document and any document referenced herein has been transmitted via facsimile to the US Patent and Trademark Office at (571) 273-8300 on 6/18/07

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